

# Garmo Forge-M8

Forge named implementation

IP-2003 · Executed digital evidence · RTL / reference evaluation

## Function and architecture

- Forge named implementation

## Integration summary

Garmo Forge-M8 provides the documented memory / compute ip responsibility: Forge named implementation Evaluate the exact GF07-002 implementation and confirm its integration contract before using it inside the target design.

## Technical record

|                                          |                                                                               |
|------------------------------------------|-------------------------------------------------------------------------------|
| <b>Canonical identity</b>                | IP-2003                                                                       |
| <b>Native implementation identity</b>    | GF07-002                                                                      |
| <b>Documented responsibility</b>         | Forge named implementation                                                    |
| <b>Recorded evidence class</b>           | Executed digital evidence                                                     |
| <b>Interface and physical parameters</b> | Bound to the selected source/configuration; not inferred from name or family. |

## Delivery and release binding

- Exact implementation and configuration manifest
- Documented interfaces and clock/reset contract
- Available reference or verification collateral
- Recorded physical views where authorized and applicable
- Integration acceptance criteria and unresolved gate list

Confirm for the selected release: Source/configuration identity, Interface and dependencies, Verification evidence, Process/library where applicable, Authorized package and acceptance criteria.

## Evidence and qualification

RELEASED PORTABLE SOFT IP — RTL, directed simulation, warning-clean lint and generic synthesis evidenced; bounded-formal collateral is partial; no PDK-bound physical closure, tapeout or silicon evidence.

Evidence shown is the Bible record for this canonical implementation, not a fresh tool rerun. External foundry acceptance, silicon measurements, standards certification and unlisted interface/physical

parameters are not inferred. Exact delivery and rights are agreed before licensing.

Canonical status: RELEASED PORTABLE SOFT IP — RTL, directed simulation, warning-clean lint and generic synthesis evidenced; bounded-formal collateral is partial; no PDK-bound physical closure, tapeout or silicon evidence.

Source basis: Bible v7.42 VERIFIED V29, IP-2003; PART LXVII - RELEASED PORTABLE SOFT-IP PORTFOLIOS (table 722, row 14).

## Evaluation and licensing

Evaluation: Review the exact recorded implementation and agreed test scope. Production: Named-design rights for the agreed qualified release. Custom work: Target integration, verification or physical qualification. Rights, pricing, support and acceptance criteria are agreed for the exact scope.

Contact: [patrick@garmolabs.com](mailto:patrick@garmolabs.com) | Product path: /ip/garmo-forge-m8-ip-2003