MEMORY ENGINEERING

Garmo Memory Compiler

Memory architecture/compiler layer spanning arrays, banking, ECC/BISR, timing/power views, wrappers, physical integration and qualification.

IP-4477 · Architecture program · Memory EDA architecture

Function and architecture

- Memory architecture/compiler layer spanning arrays
- banking
- ECC/BISR
- timing/power views
- wrappers
- physical integration and qualification

Integration summary

Garmo Memory Compiler: Memory architecture/compiler layer spanning arrays, banking, ECC/BISR, timing/power views, wrappers, physical integration and qualification. Delivered as Memory EDA architecture. Integrate the selected version at its documented input/output boundary; retain source and configuration identity with each result. The exact callable API, dependencies and runtime environment are supplied with the controlled release; this public description is not a substitute for that release contract.

Technical record

Canonical identity	IP-4477
Responsibility	Memory architecture/compiler layer spanning arrays, banking, ECC/BISR, timing/power views, wrappers, physical integration and qualification.
Delivery boundary	Memory EDA architecture
Evidence scope	Architecture program
Engineering boundary	Memory architecture/compiler layer spanning arrays, banking, ECC/BISR, timing/power views, wrappers, physical integration and qualification.
Deployment	Memory EDA architecture;
Operational scope	The named architecture and its implementation scope are not a blanket shipping-product, physical qualification or production-deployment claim. The exact release controls evaluation.

Delivery and release binding

- Versioned package and dependency lock
- Deployment environment and access controls
- Input/output schemas and integration examples
- Evaluation scope, support and update policy

Confirm for the selected release: Environment and dependencies, Version / build hash, API compatibility, License and support scope.

Evidence and qualification

ENGINEERING BUILD SPEC / CANONICAL GSP MODULE IDENTITY. Production implementation, authoritative model binding, correlation, physical/lab/silicon evidence and external certification remain release-specific.

The named architecture and its implementation scope are not a blanket shipping-product, physical qualification or production-deployment claim. The exact release controls evaluation.

Canonical status: ENGINEERING BUILD SPEC / CANONICAL GSP MODULE IDENTITY. Production implementation, authoritative model binding, correlation, physical/lab/silicon evidence and external certification remain release-specific.

Source basis: Bible v7.42, IP-4477

Evaluation and licensing

Evaluation: Evaluate the named software release. Production: Team or enterprise deployment. Custom work: Workflow and API integration. Rights, pricing, support and acceptance criteria are agreed for the exact scope.

Contact: patrick@garmolabs.com | Product path: /software/garmo-memory-compiler