

Autonomous Foundry Feedback Loop

Turns measured silicon/process behavior into versioned confidence-bound models and next-design/runtime feedback.

SYSTEM-07 · Integrated reference · System architecture + source

Function and architecture

- Characterization sequencer
- monitor aggregation
- model fitter
- confidence engine
- yield feedback
- guardband compiler

Integration summary

Integration scope, interfaces and qualification are agreed for the selected release.

Technical record

Canonical identity	SYSTEM-07
Responsibility	Turns measured silicon/process behavior into versioned confidence-bound models and next-design/runtime feedback.
Delivery boundary	System architecture + source
Evidence scope	Integrated reference

Delivery and release binding

- Constituent release manifest
- Topology and physical integration assumptions
- System software and orchestration boundaries
- Qualification scope and system-level acceptance

Confirm for the selected release: Environment and dependencies, Version / build hash, API compatibility, License and support scope.

Evidence and qualification

The R2 record contains separate domain tiles, fabric, SoC and chip-top identities, with reference/invariant campaigns and static release audits.

The system source/reference release is not a physical machine delivery or new signoff-class silicon.

Source basis: Bible v7.42 §141, independent system 7

Evaluation and licensing

Evaluation: System architecture evaluation. Production: Platform integration program. Custom work: Customer-specific system composition. Rights, pricing, support and acceptance criteria are agreed for the exact scope.

Contact: patrick@garmolabs.com | Product path: /system/autonomous-foundry-feedback-loop